

IN THE SPECIFICATION



Please replace the Title on page 1, lines 2-3, with the following amended Title:

LOCAL MEMORY MANAGEMENT SYSTEM WITH PLURAL PROCESSORS

Please amend the specification at page 3, lines 2-24, as follows:

An object of the present invention is to provide an information processing system and a memory managing method capable of efficiently executing a plurality of threads ~~with efficiency~~ in parallel to each other using a plurality of processors.

According to an embodiment of the present invention, there is provided an information processing system comprising a first processor having a first local memory, a second processor having a second local memory, a third processor having a third local memory, means for mapping one of the second local memory and the third local memory in part of an effective address space of a first thread to be executed by the first processor, the one of the second local memory and the third local memory being the local memory of a corresponding one of the second processor and the third processor, which executes a second thread interacting with the first thread, and means for changing a local memory to be mapped in part of the effective address space of the first thread from the one of the second local memory and the third local memory which is to be mapped in part of the effective address space of the first thread to the other of the second local memory and the third local memory when a processor that executes the second thread is changed from the one of the second processor and the third processor that executes the second thread is changed to the other of the second processor and the third processor.